




IPW

PATENT
Docket No. Intel/17226

IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE

Applicants: Nanja et al.)	I hereby certify that this paper is
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Filed: September 10, 2003)	Box 1450, Alexandria, VA 22313-
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)	
For: "Methods and Apparatus for Dynamic)	March 16, 2005
Best Fit Compilation of Mixed Mode)	
Instructions")	
)	Mark C. Zimmerman
Group Art Unit: 2122)	Registration No. 44,006
)	Attorney for Applicant(s)
Examiner: Not yet assigned)	

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The documents listed on the enclosed PTO Form-1449 are submitted pursuant to
37 CFR §§ 1.56, 1.97, and 1.98. Copies of these documents enclosed.

Submitted herewith is a copy of an Official Search Report of the European Patent
Office in parent/counterpart foreign application Number PCT/US2004/029083 filed
August 9, 2004.

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

HANLEY, FLIGHT & ZIMMERMAN, LLC.
USPTO Customer Number 34431
20 N. Wacker Drive
Suite 4220
Chicago, Illinois 60606
(312) 580-1020

By:



Mark C. Zimmerman
Registration No.: 44,006

March 16, 2005

Attorneys for Intel Corporation

Form PTO-1449 (Modified)

U.S. Department of Commerce
Patent and Trademark Office

Atty. Docket No.

Serial No.

Intel/17226

10/659,457

Applicant

Nanja et al.

Filing Date

09-10-03

Group Art Unit

2122

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	<i>International Search Report for International Patent Application Serial Number PCT/US2004/029083</i> ; mailed February 7, 2005; 4 pages.
	C02	<i>Written Opinion of the International Searching Authority for International Patent Application Serial Number PCT/US2004/029083</i> ; mailed February 7, 2005; 7 pages.
	C03	Krishnaswamy et al., " <i>Profile Guided Selection of ARM and Thumb Instructions</i> ," Sigplan Notices ACM USA, Vol. 37, No. 7, July 2002; pages 56-64.
	C04	Krishnaswamy et al., " <i>Mixed-Width Instructions Sets</i> ," Communications of the Association for Computing Machinery, Association for Computing Machinery, New York, Vol. 46, No. 8, August 2003; pages 47-52.
	C05	Fischer et al., " <i>Optimal Mixing of 16 bit and 32-bit computer codes by means of 'Codebalance'</i> ," , Elektronik Weka-Fachzeitschriften Germany, Vol. 48, No. 19, September 21, 1999; pages 96-99.
	C06	Halambi et al., " <i>An Efficient Compiler Technique for Code Size Reduction Using Reduced Bit-Width ISAs</i> ," Proceedings 2002 Design, Automations and Test in Europe Conference and Exhibition IEEE Computer Society, Los Alamitos, California, March 2002; pages 402-408.
	C07	Delsart et al., " <i>JCOD: a Lightweight Modular Compilation Technology for Embedded Java</i> ," Lecture Notes in Computer Science, Springer Verlag, New York, New York, Vol. 2491, October 2002; pages 197-212.
	C08	Clark D: " <i>Mobile Processors Begin to Grow Up</i> ," Computer IEEE Computer Society USA, Vol. 35, No. 3, March 2002; pages 22-25.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.